

Infineon Docket No. 2003P52888US  
OC Docket No. INFN/0032  
Express Mail No.

## **METHOD AND CIRCUIT CONFIGURATION FOR REFRESHING DATA IN A SEMICONDUCTOR MEMORY**

### **ABSTRACT**

Methods and circuit configurations for refreshing data in a semiconductor memory device in which refresh operations are performed for a limited number of rows. The limited number of rows may include only those rows that contain valid data, for example, as determined by monitored write operations.